

FIG. 1 (PRIOR ART)

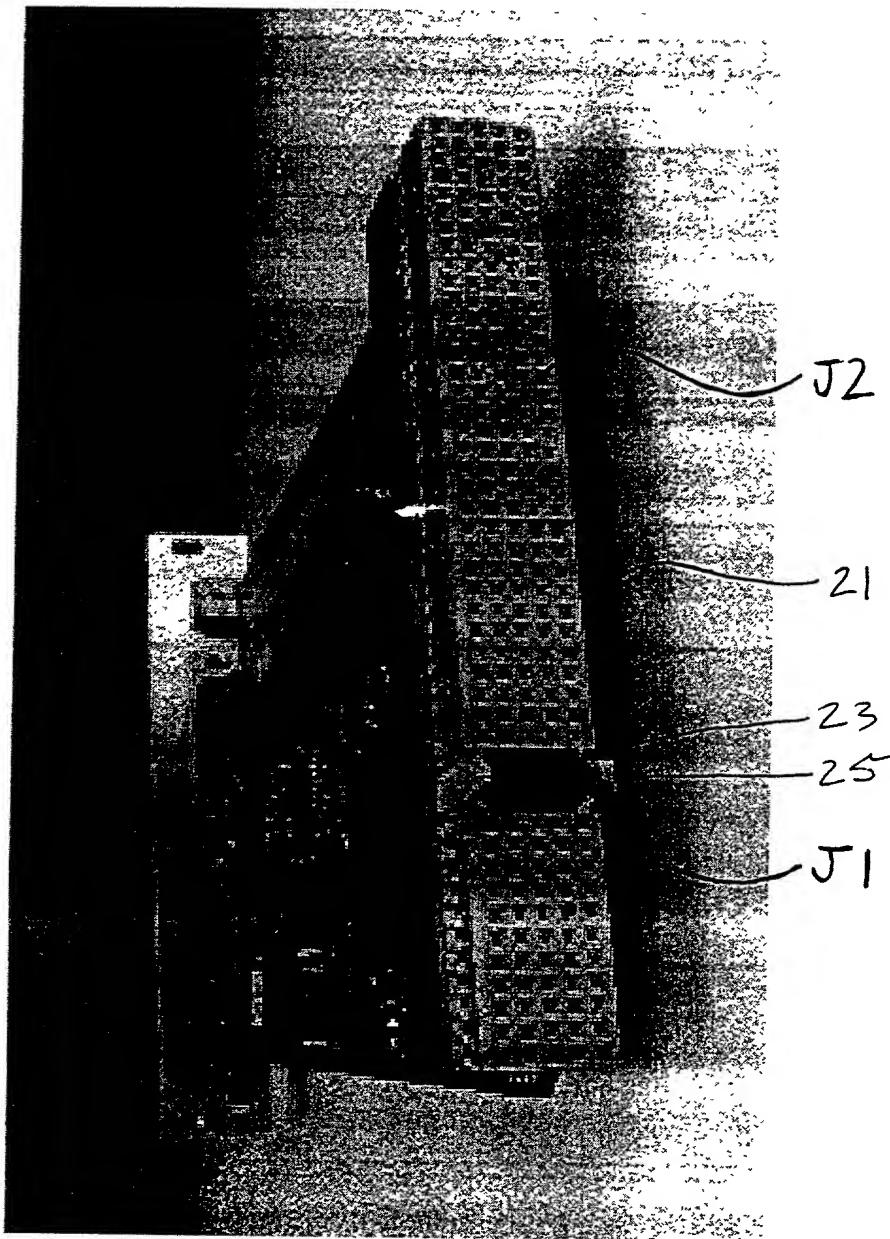
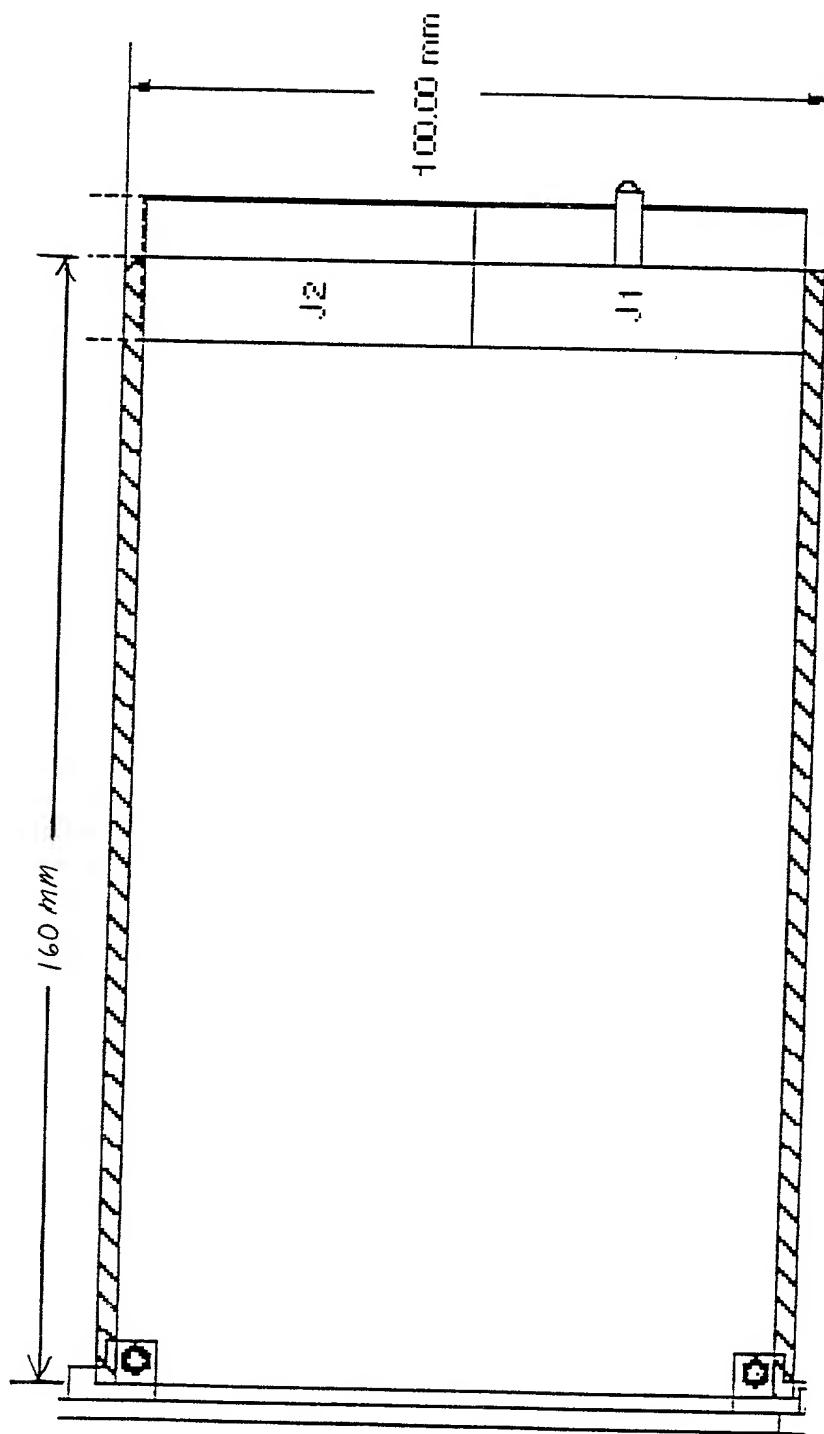


FIG. 2 (Prior Art)

FIG. 3 (Prior Art)



25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND	
24	GND	AD[1]	5V	V(I/O)	AD[0]	ACK64#	GND	
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND	J1
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND	
21	GND	3.3V	AD[9]	AD[8]	M66EN	C/BE[0]#	GND	
20	GND	AD[12]	GND	V(I/O)	AD[11]	AD[10]	GND	C
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND	O
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND	N
17	GND	3.3V	SDONE	SBO#	GND	PERR#	GND	
16	GND	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	GND	E
15	GND	3.3V	FRAME#	IRDY#	GND	TRDY#	GND	C
12-14				KEY AREA				T
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND	O
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND	R
9	GND	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	GND	
8	GND	AD[26]	GND	V(I/O)	AD[25]	AD[24]	GND	
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND	
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND	
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND	
4	GND	BRSVP1A4	GND	V(I/O)	INTP	INTS	GND	
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND	
2	GND	TCK	5V	TMS	TDO	TDI	GND	
1	GND	5V	-12V	TRST#	+12V	5V	GND	
Pin	Z	A	B	C	D	E	F	

FIG. 4

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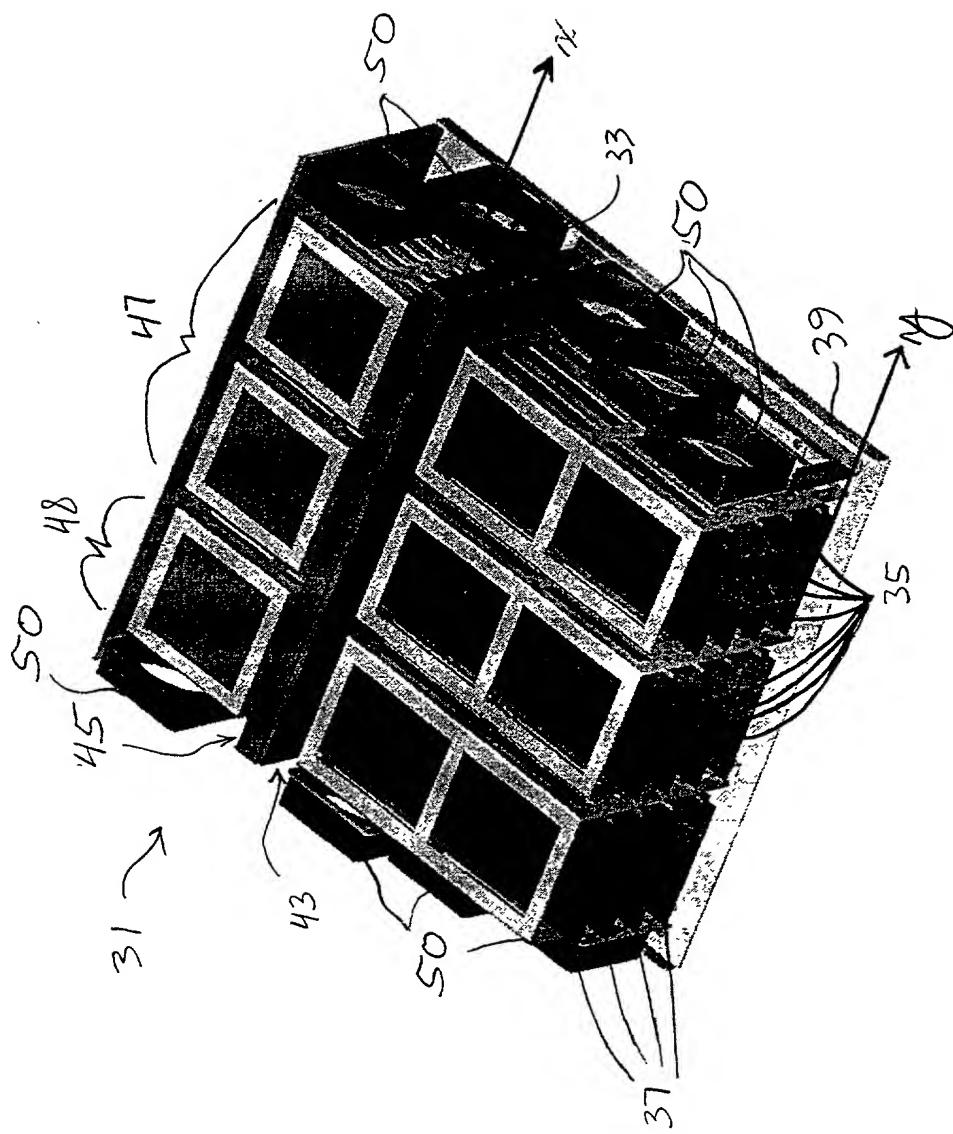
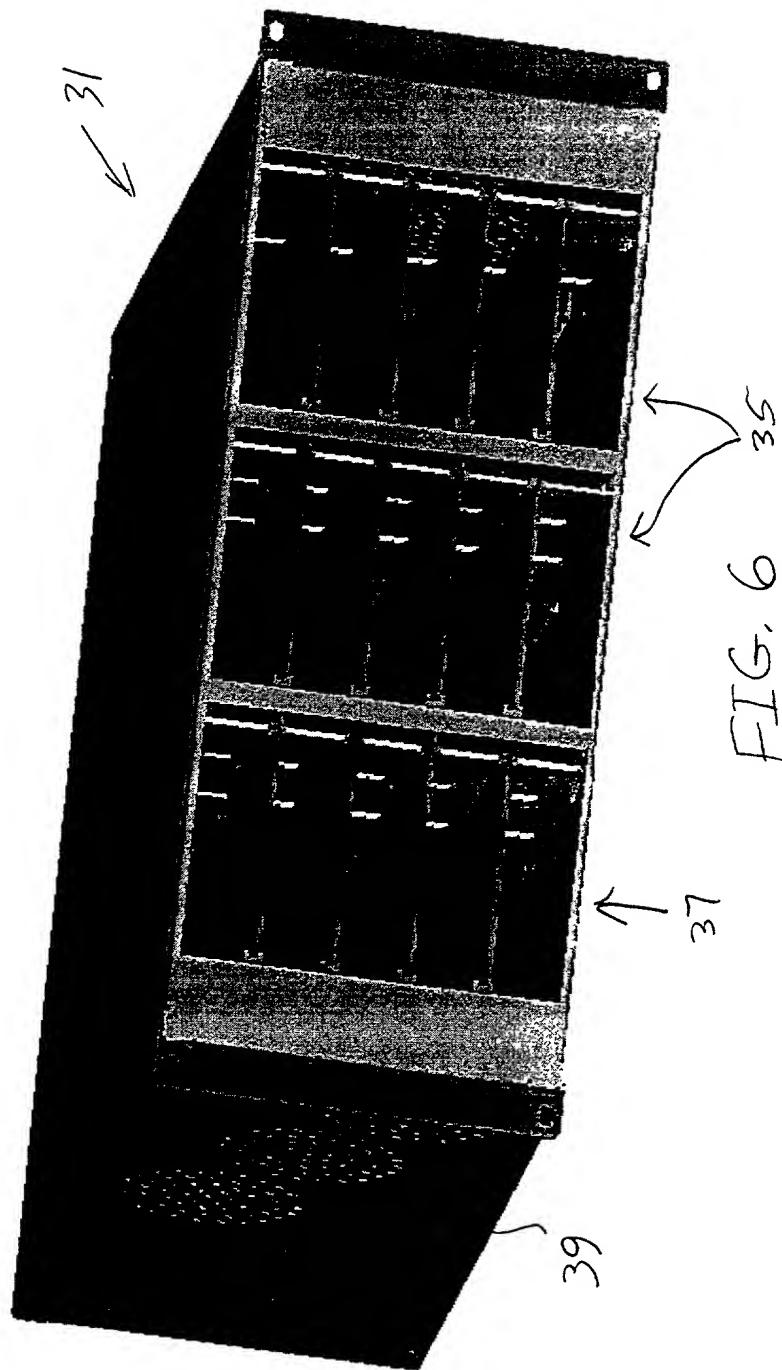
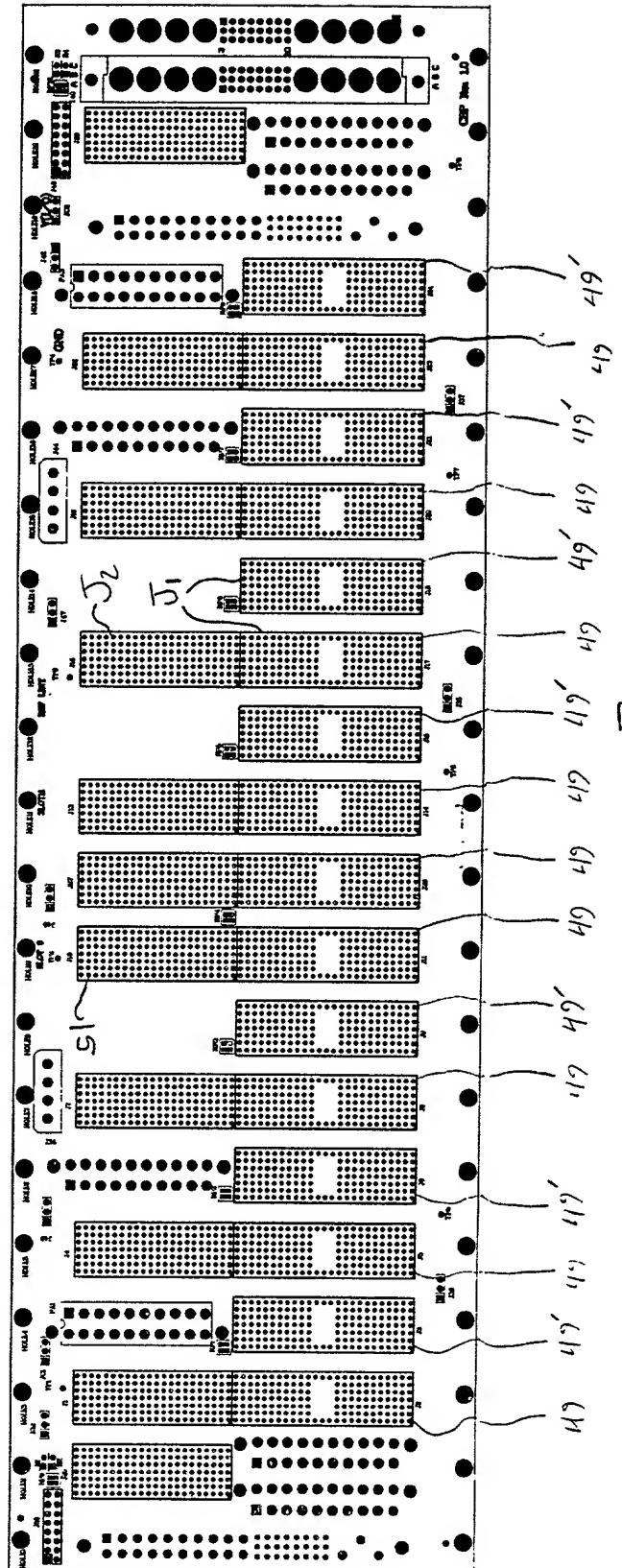


FIG. 5



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25	GND	5V	3.3V	ENUM#	REQ64#	5V	GND	J1 C O N N E C T O R
24	GND	ACK64#	AD[0]	V(I/O)	5V	AD[1]	GND	
23	GND	AD[2]	5V	AD[3]	AD[4]	3.3V	GND	
22	GND	AD[5]	AD[6]	3.3V	GND	AD[7]	GND	
21	GND	C/BE[0]#	M66EN	AD[8]	AD[9]	3.3V	GND	
20	GND	AD[10]	AD[11]	V(I/O)	GND	AD[12]	GND	
19	GND	AD[13]	GND	AD[14]	AD[15]	3.3V	GND	
18	GND	C/BE[1]#	PAR	3.3V	GND	SERR#	GND	
17	GND	PERR#	GND	SBO#	SDONE	3.3V	GND	
16	GND	LOCK#	STOP#	V(I/O)	GND	DEVSEL#	GND	
15	GND	TRDY#	GND	IRDY#	FRAME#	3.3V	GND	
12-14				KEY AREA				
11	GND	C/BE[2]#	GND	AD[16]	AD[17]	AD[18]	GND	
10	GND	AD[19]	AD[20]	3.3V	GND	AD[21]	GND	
9	GND	AD[22]	GND	AD[23]	IDSEL	C/BE[3]#	GND	
8	GND	AD[24]	AD[25]	V(I/O)	GND	AD[26]	GND	
7	GND	AD[27]	GND	AD[28]	AD[29]	AD[30]	GND	
6	GND	AD[31]	CLK	3.3V	GND	REQ#	GND	
5	GND	GN#	GND	RST#	BRSVP1 B5	BRSVP1A 5	GND	
4	GND	INTS	INTP	V(I/O)	GND	BRSVP1A 4	GND	
3	GND	INTD#	5V	INTC#	INTB#	INTA#	GND	
2	GND	TDI	TDO	TMS	5V	TCK	GND	
1	GND	5V	+12V	TRST#	-12V	5V	GND	
Pin	F	E	D	C	B	A	Z	

FIG.8

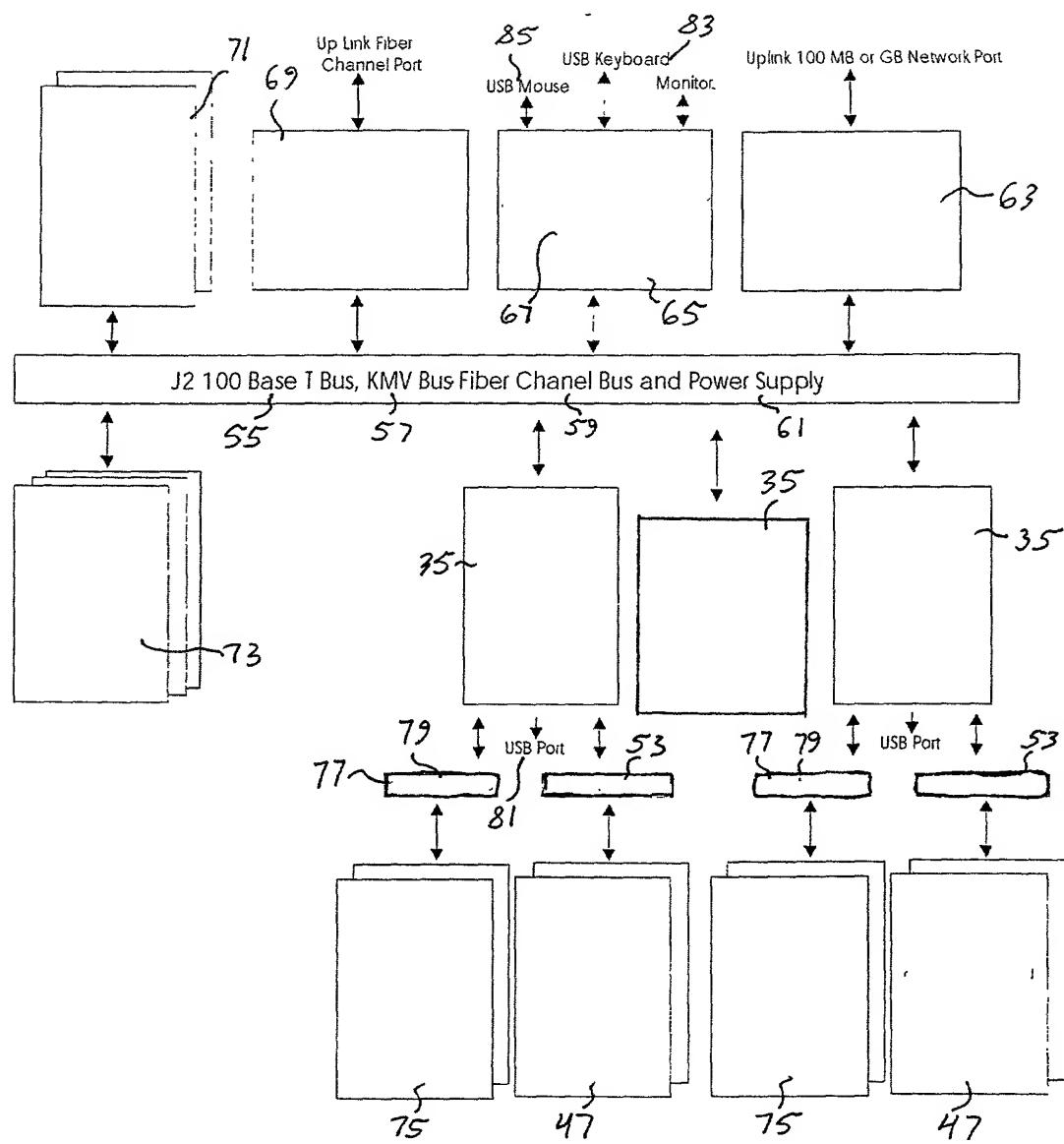


FIG. 9

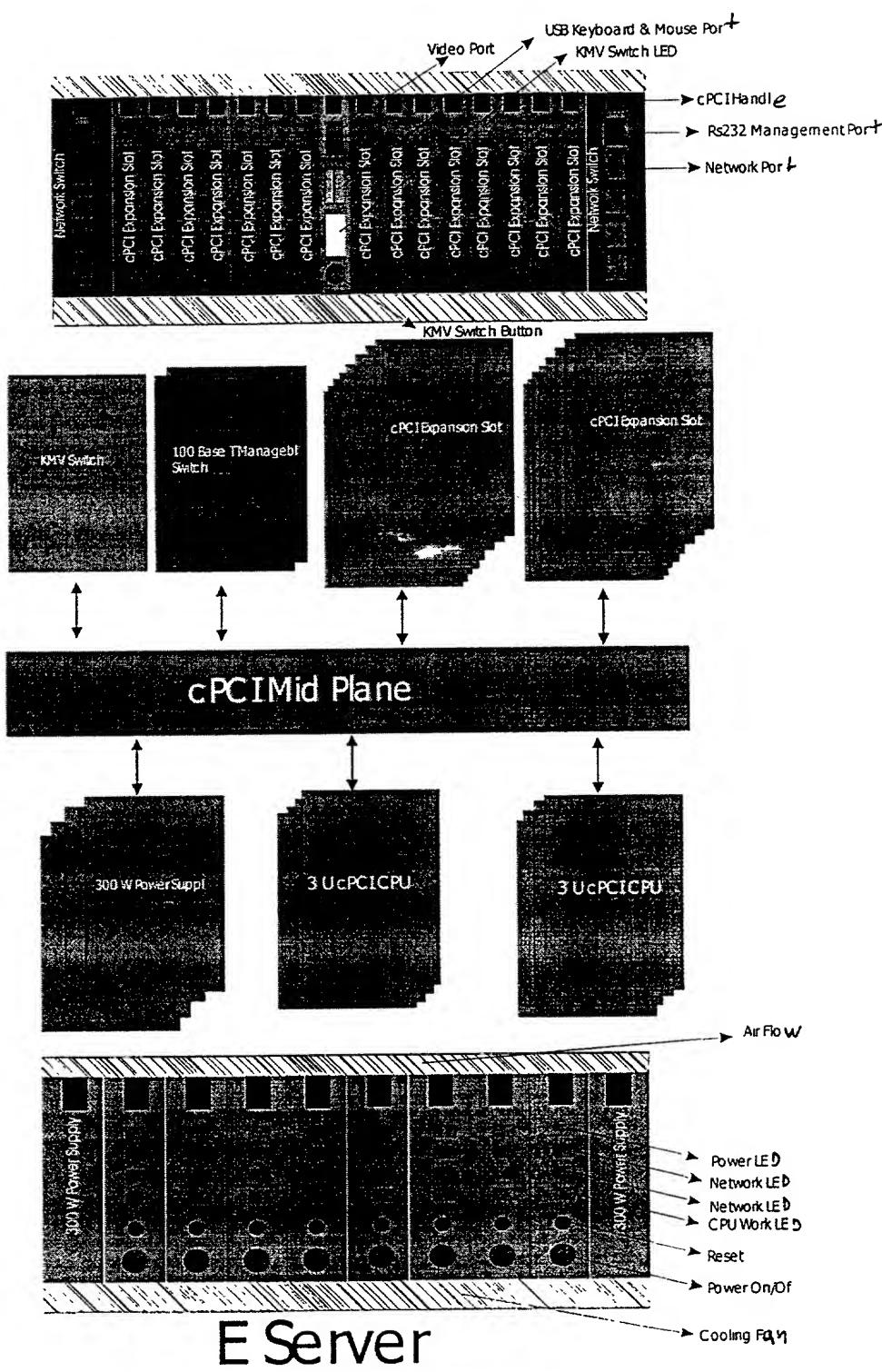


FIG. 10

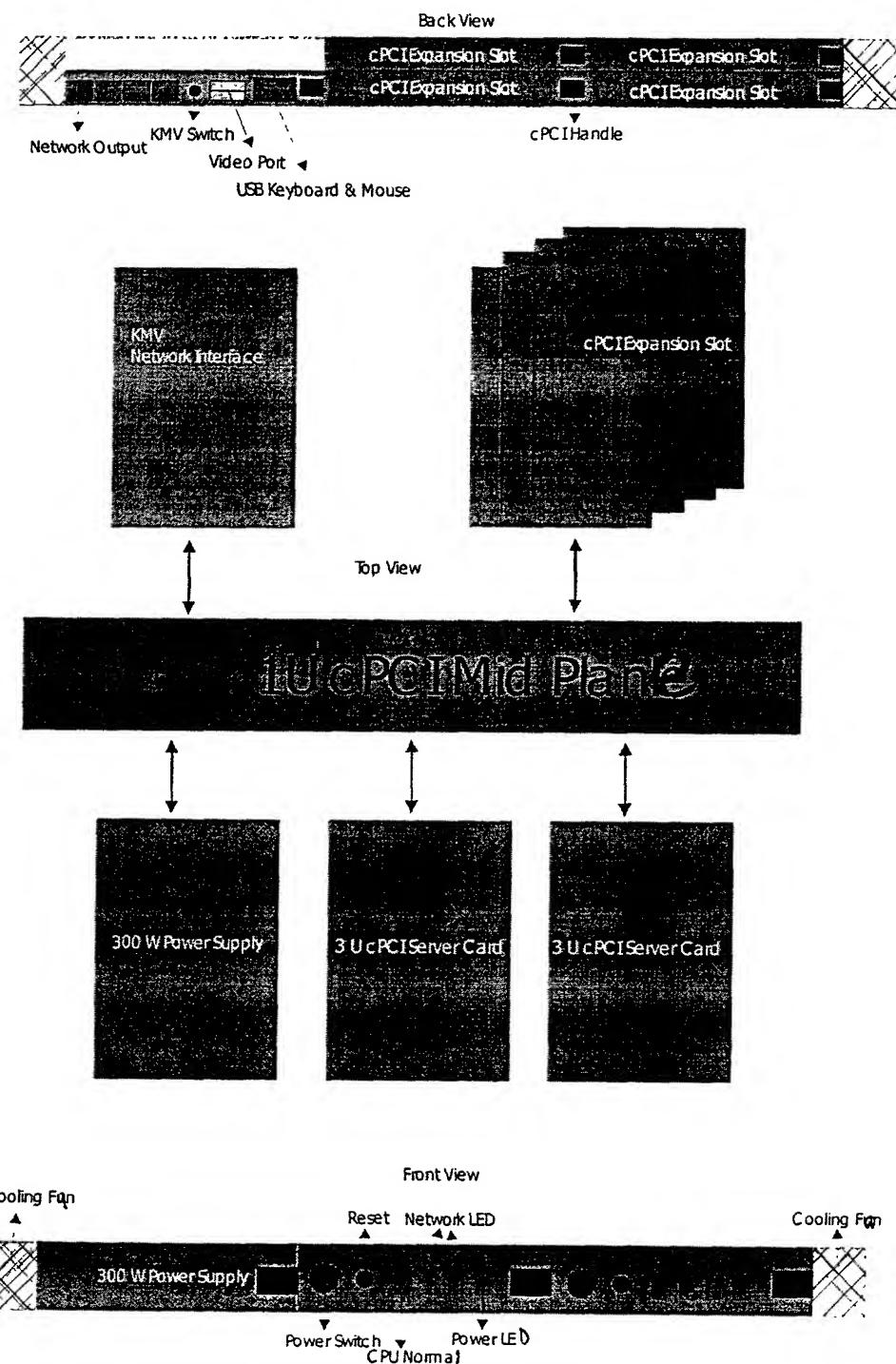


FIG. 11

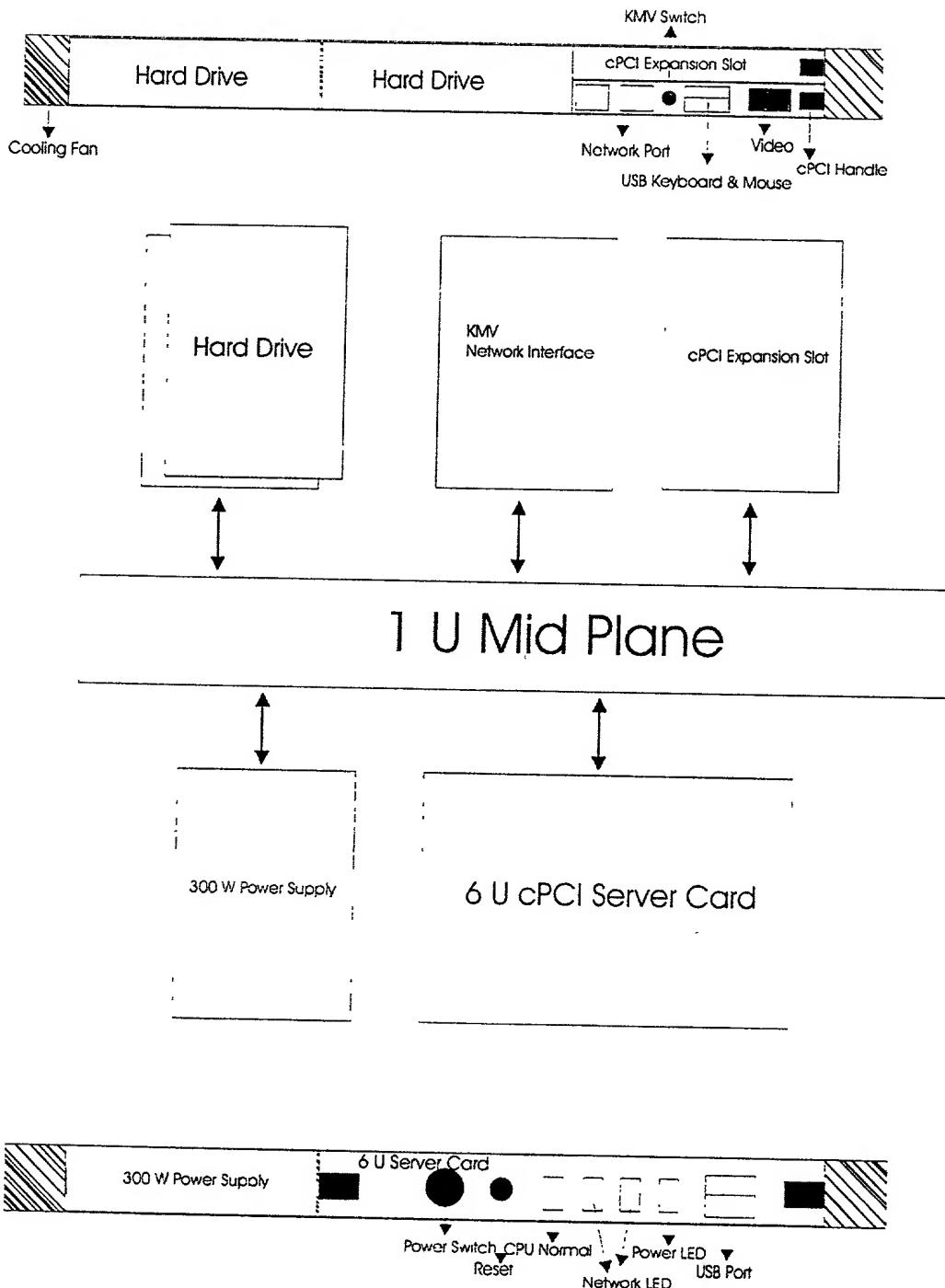


FIG. 12

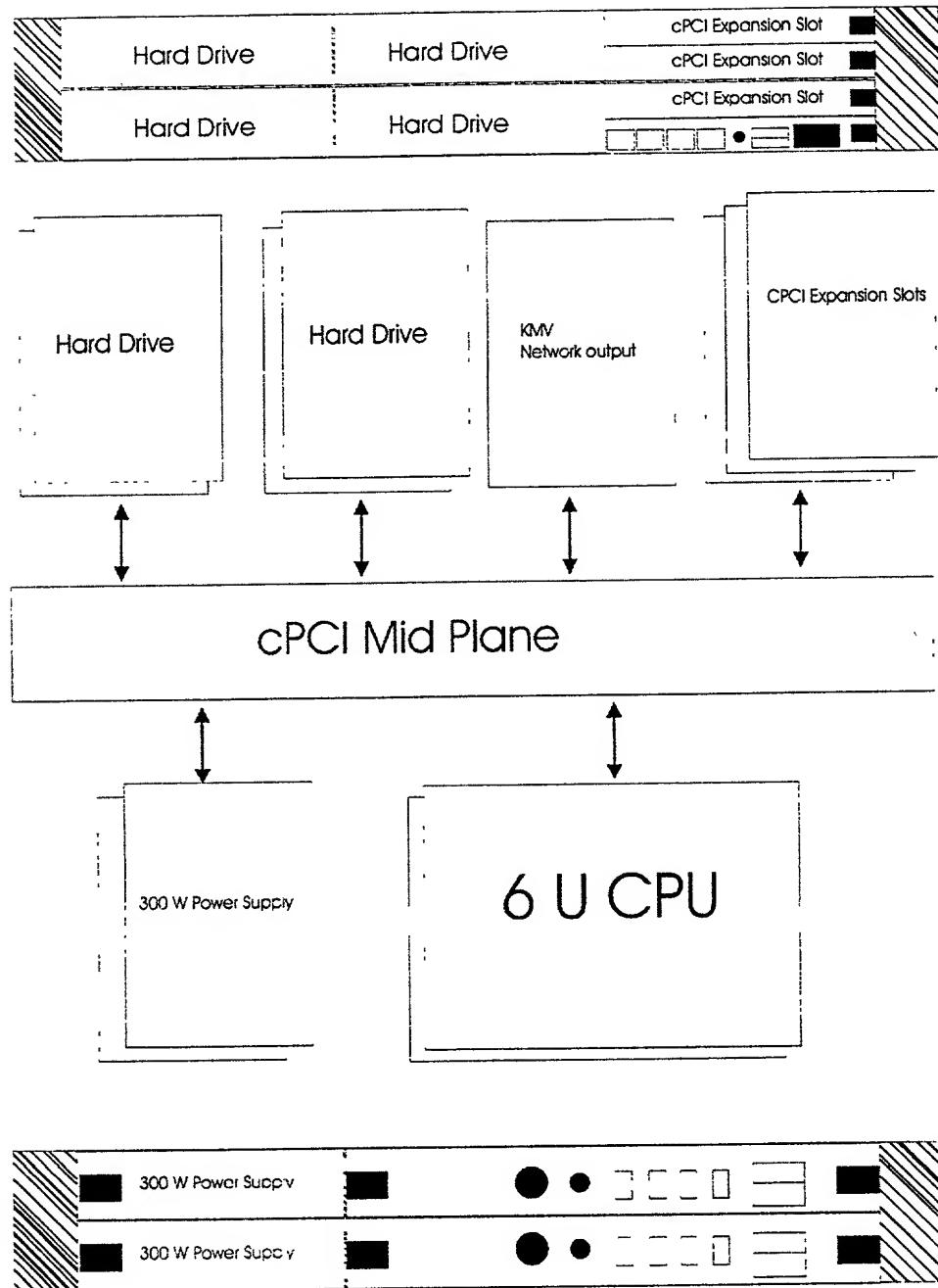


FIG. 13

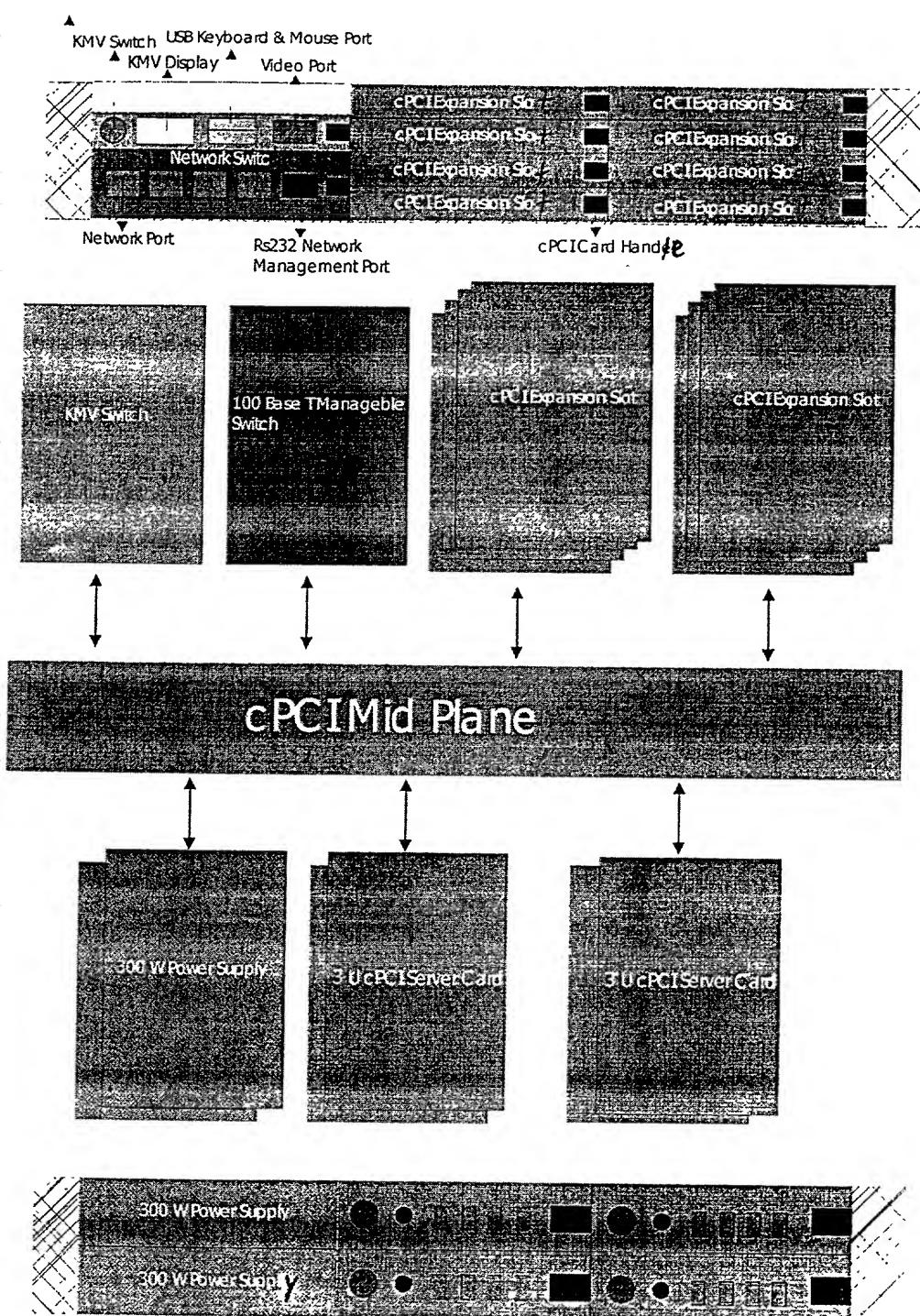


FIG. 14

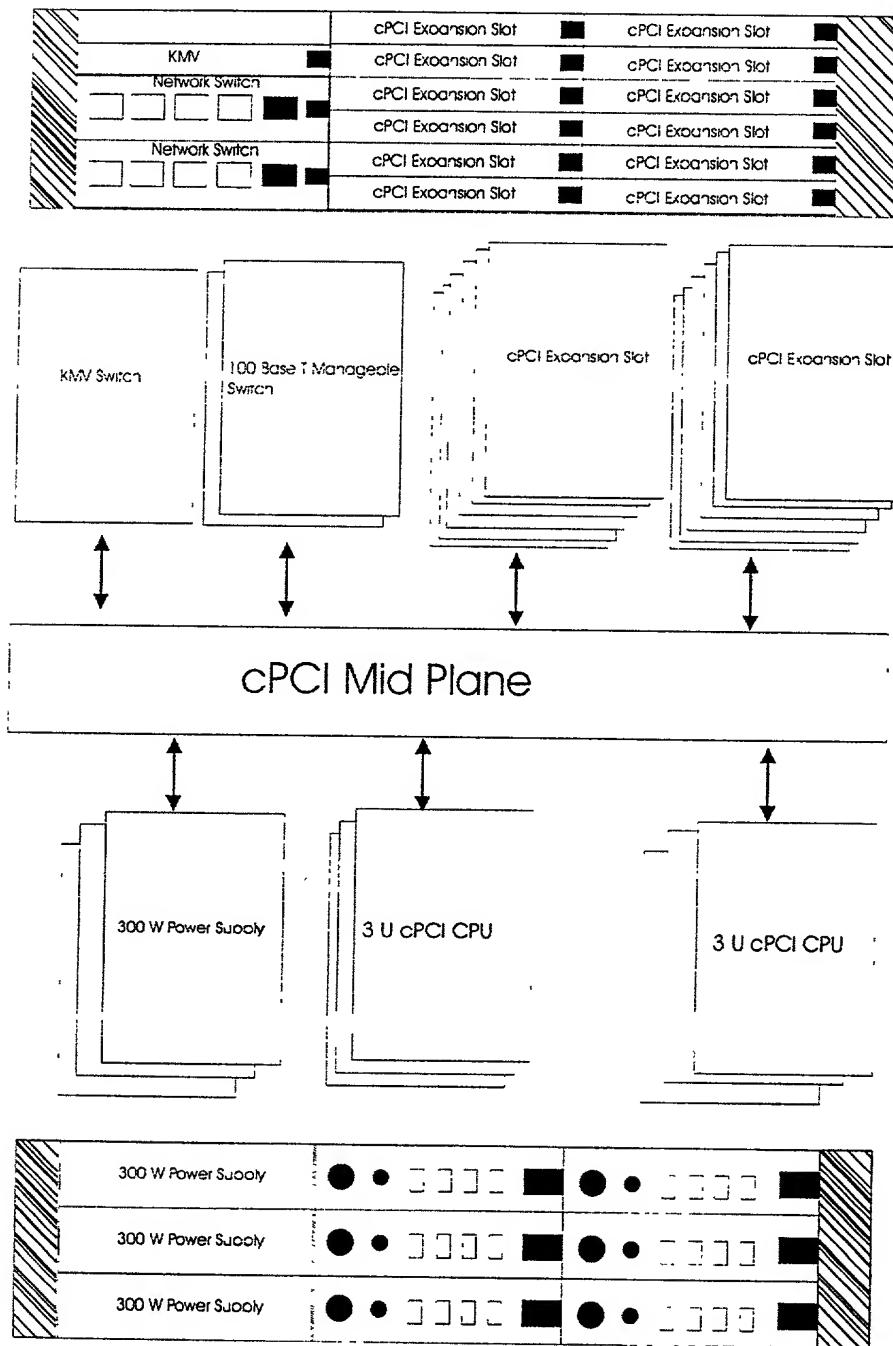
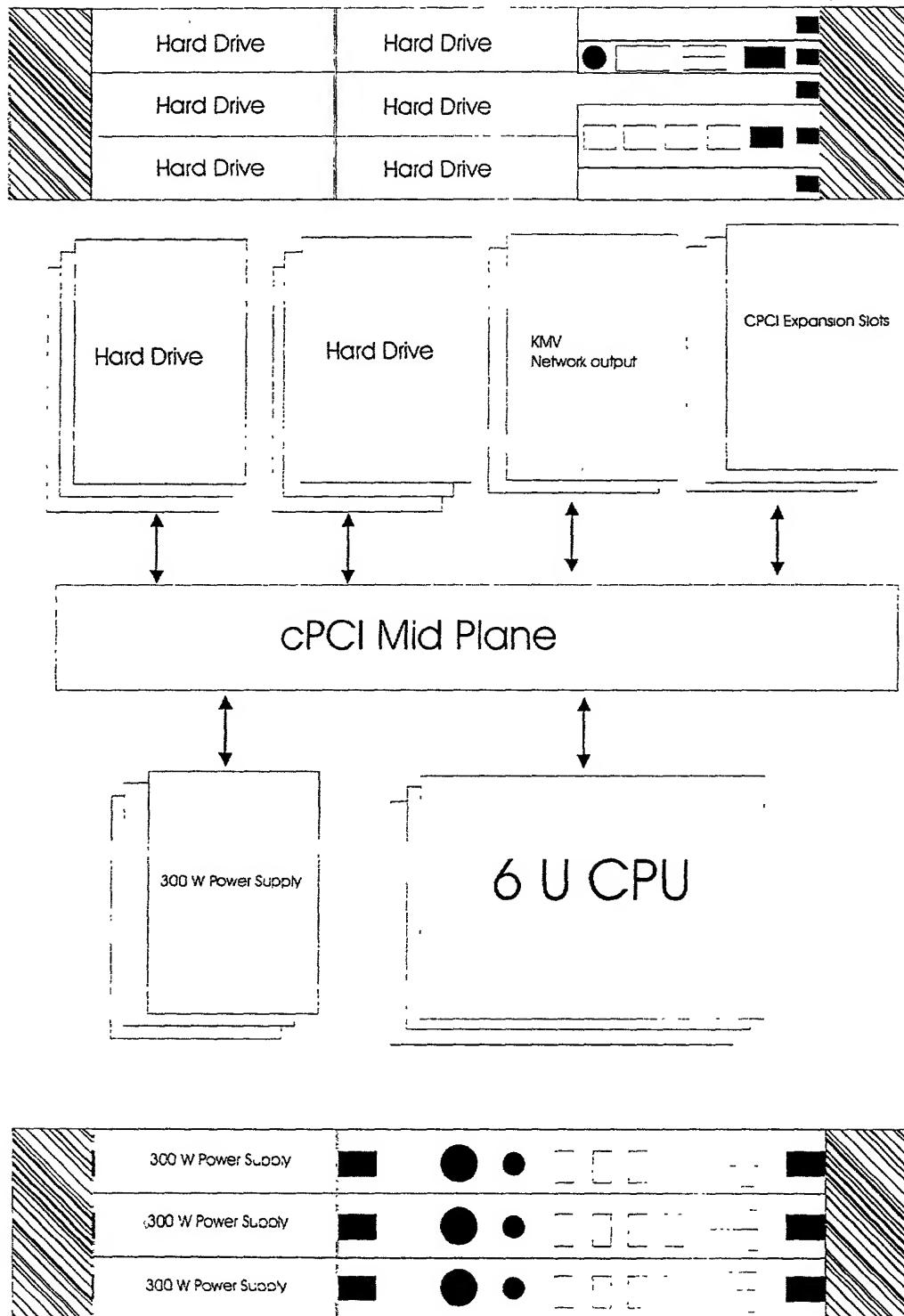


FIG. 15



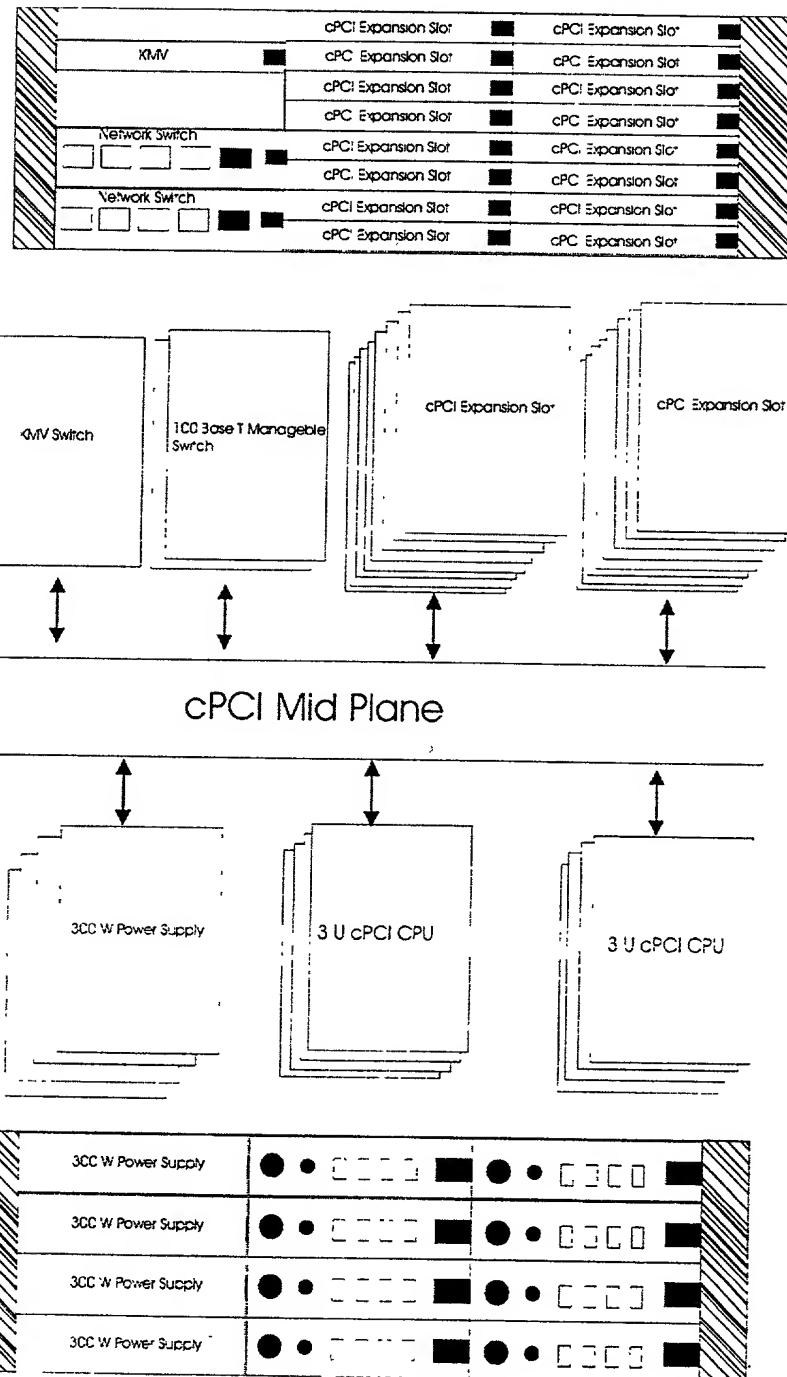


FIG. 17

CONFIDENTIAL INFORMATION

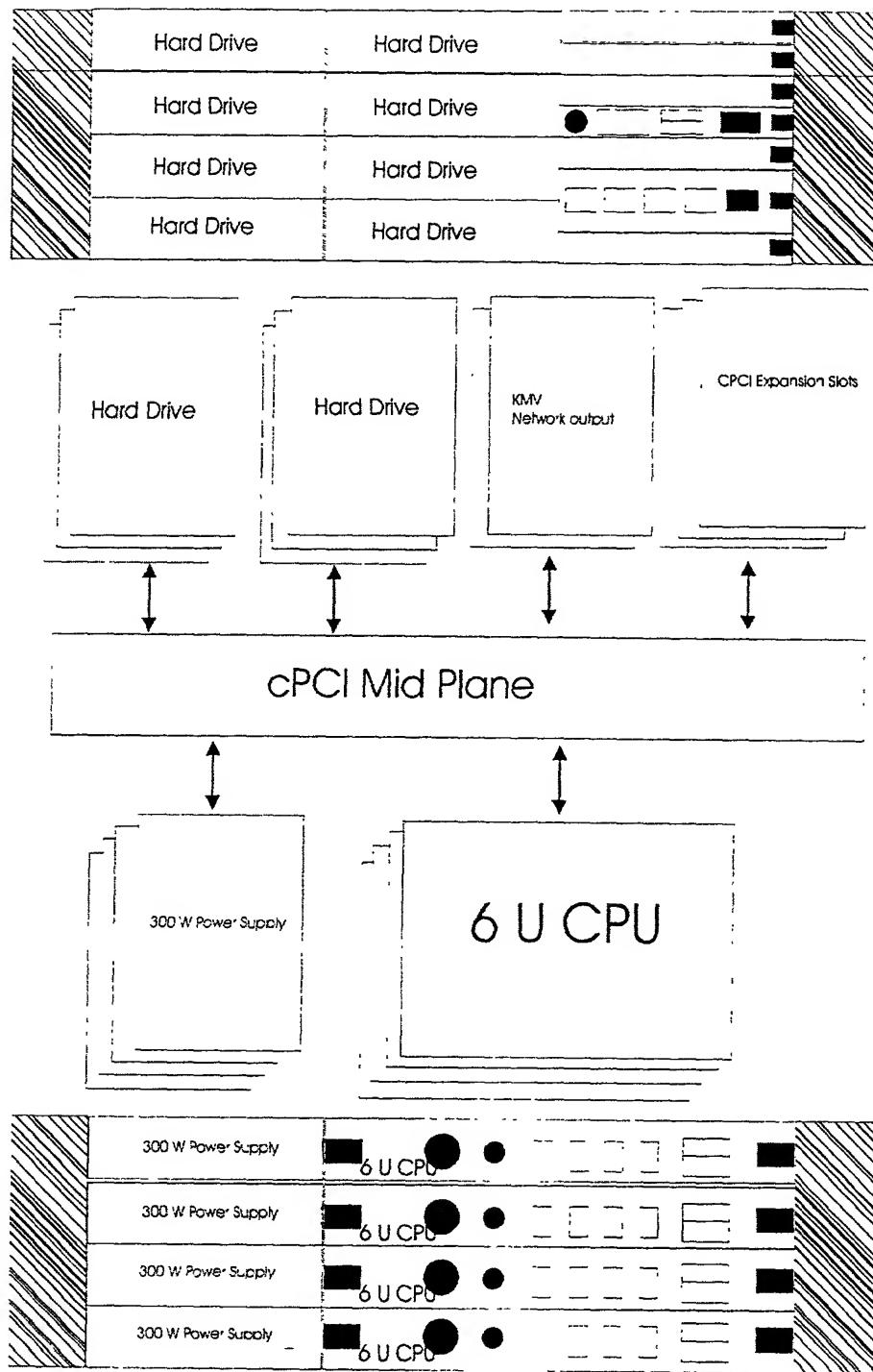


FIG. 18

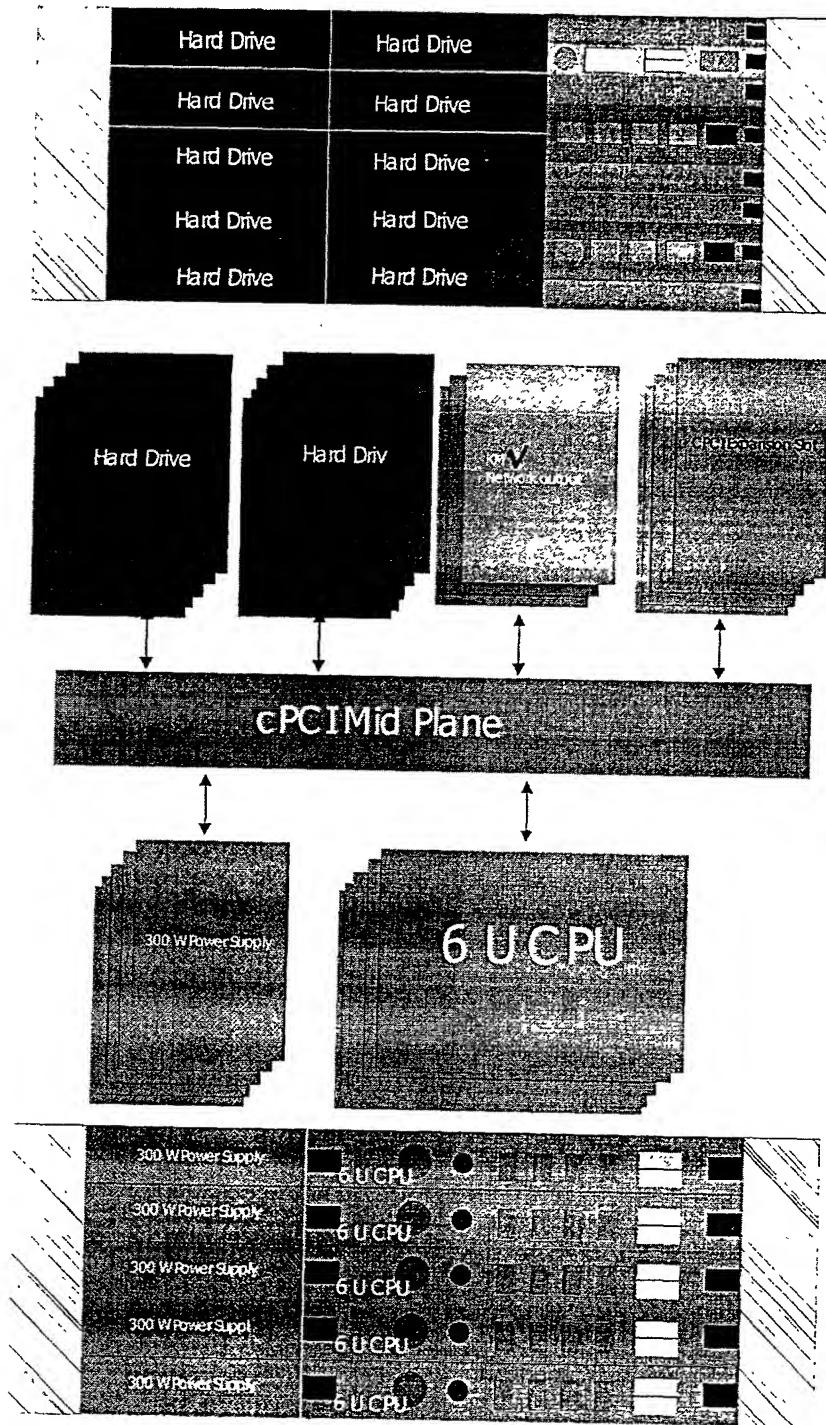


FIG. 19

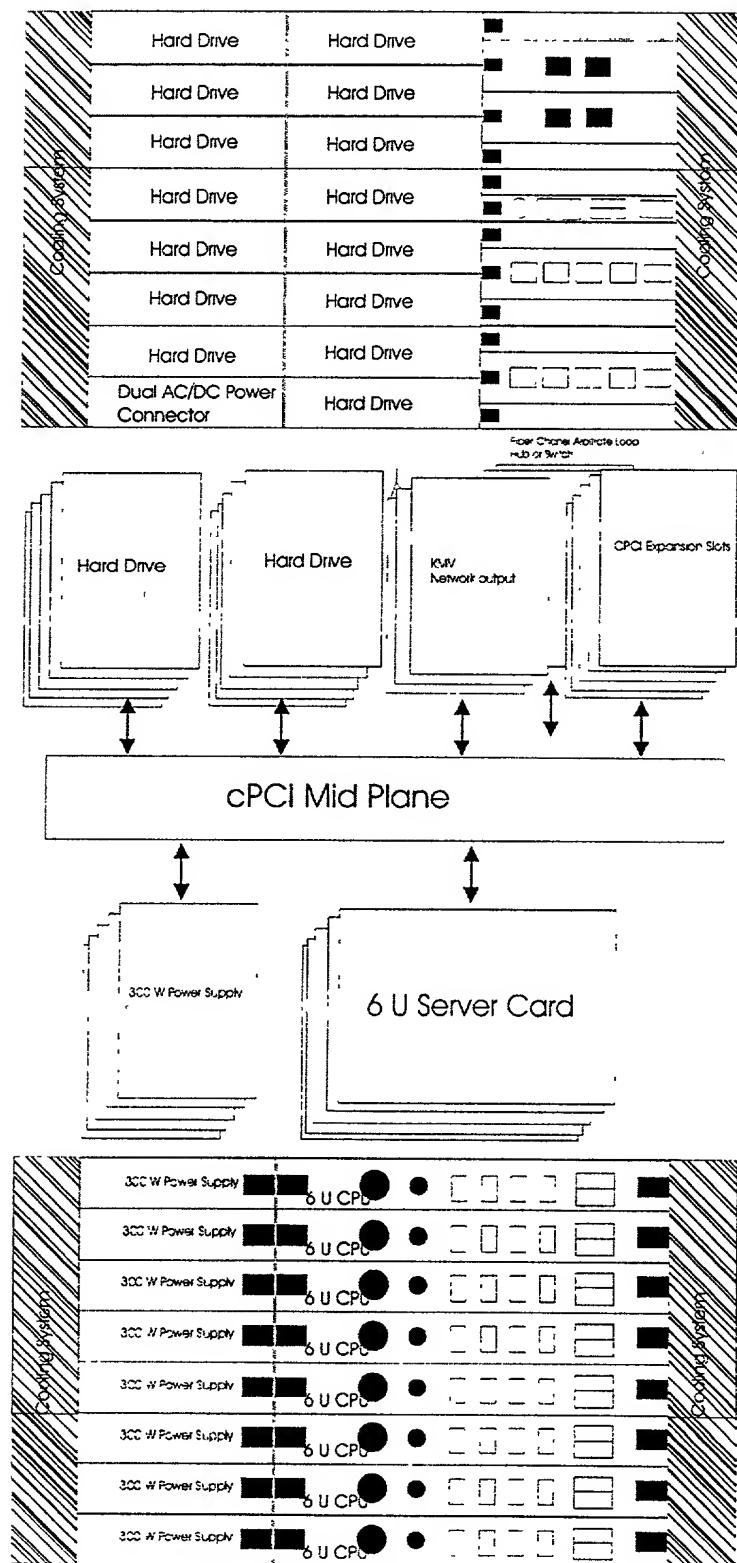
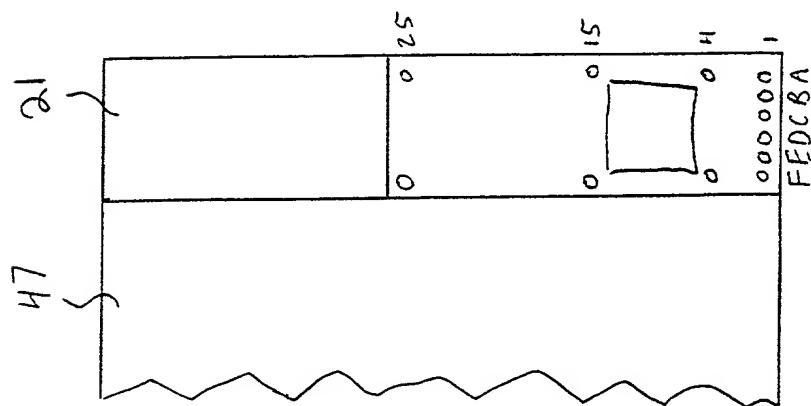
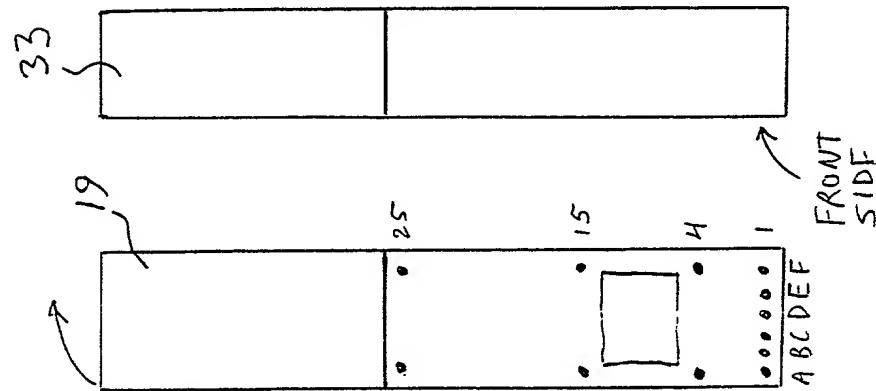
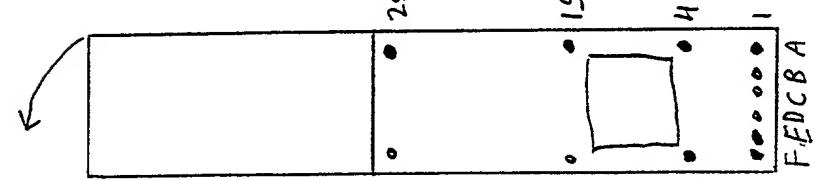
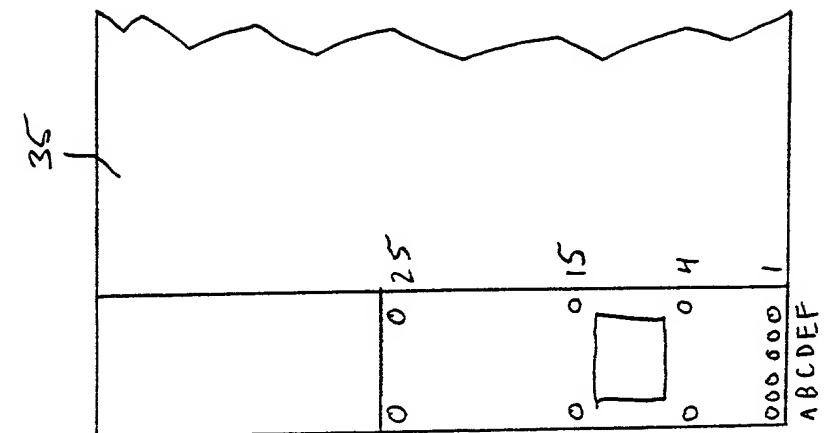


FIG. 20

Fig. 21



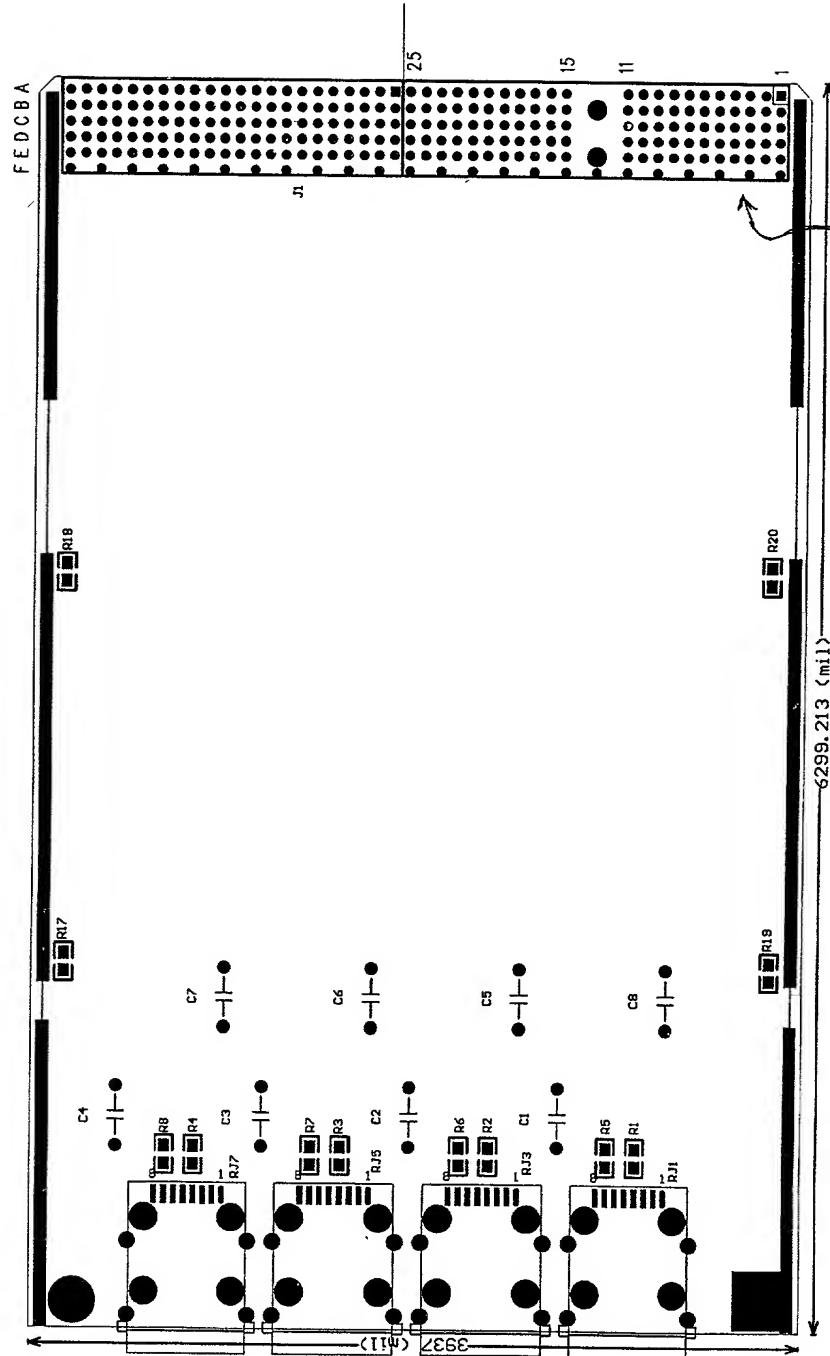
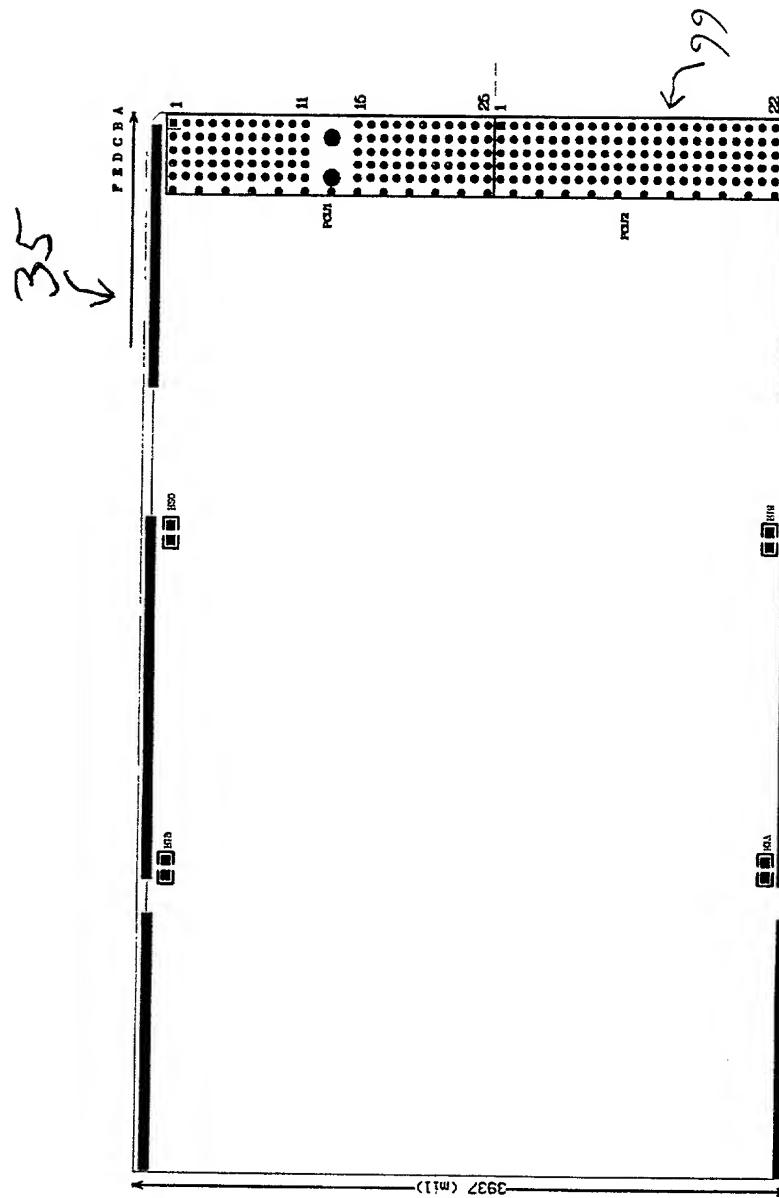


Fig. 22

Fig. 23



22	GND	GND	GND	GND	GND	GND	GND	
21	GND	?3	GND	?18	?24	SMBDAT	GND	
20	GND	?2	?7	?17	GND	SMCLK	GND	
19	GND	GND	GND	?15	?22	CUV1+	GND	
18	GND	MDCLK	?5	?14	GND	GND	GND	
17	GND	GND	GND	?13	?21	CUV1-	GND	
16	GND	MDDAT	MVSYNC	?12	GND	GND	GND	
15	GND	GND	GND	?11	?20	?28	GND	
14	GND	RSTSW#	MHSYNC	VCC5IN	GND	?27	GND	
13	GND	GND	GND	VIO	ER1+	GND	GND	
12	GND	CUV0-	MB	VCC5IN	GND	ER2+	GND	
11	GND	GND	GND	VIO	ER1-	GND	GND	
10	GND	CUV0+	MG	VCC5IN	GND	ER2-	GND	
9	GND	GND	GND	VIO	ET1+	GND	GND	
8	GND	MUSDATA	MR	VCC5IN	GND	ET2+	GND	
7	GND	GND	GND	VIO	ET1-	GND	GND	
6	GND	MUSCLK	?10	VCC5IN	GND	ET2-	GND	
5	GND	GND	?9	VIO	?26	GND	GND	
4	GND	VIO	?8	VCC5IN	GND	CUV3-	GND	
3	GND	?4	GND	?19	?25	GND	GND	
2	GND	?1	?6	?16	?23	CUV3+	GND	
1	GND	PCICLK4	GND	PREQ# 3	PGNT# 3	GND	GND	
Pin	Z	A	B	C	D	E	F	

FIG. 24